Listing of Claims:

1. (original) A semiconductor device including a contact pad and circuit metallization on the surface of an integrated circuit chip, comprising:

a stack of protection layers over the surface of said chip, said stack comprising a first inorganic layer on said surface, a polymer layer on said first inorganic layer, and a second inorganic layer on said polymer layer;

a window in said stack of layers exposing said metallization on said integrated circuit chip;

a patterned seed metal layer on said metallization in said window and on said second inorganic layer around said window; and

buffer metal layer positioned on said seed metal layer.

- 2. (original) The device according to Claim 1 wherein said interconnecting metallization comprises copper.
- 3. (original) The device according to Claim 1 wherein said first inorganic layer comprises silicon nitride.
- 4. (original) The device according to Claim 1 wherein said first inorganic layer is selected from a group consisting of silicon nitride, silicon oxynitride, silicon carbide, polyimide, and stacked layer of said materials.
- 5. (original) The device according to Claim 1 wherein said first inorganic layer has a thickness in the range from about 0.5 to 2 μ m.
- 6. (original) The device according to Claim 1 wherein said polymer layer comprises benzocyclobutene or polybenzoxazole.
- 7. (original) The device according to Claim 1 wherein said polymer layer is selected from a group consisting of polyimides, polyamic acids, polybenzoxazoles, benzocyclobutenes, polybenzocyclobutenes, and polysiloxanes.
- 8. (original) The device according to Claim 1 wherein said polymer layer has a thickness of the range from about 3 to 10 μm .

- 9. (original) The device according to Claim 1 wherein said second inorganic layer comprises silicon dioxide.
- 10. (original) The device according to Claim 1 wherein said second inorganic layer is a dielectric selected from a group consisting of silicon dioxide, silicon nitride, silicon oxynitride, and stacked layers thereof.
- 11. (original) The device according to Claim 1 wherein said second inorganic layer has a thickness from about 0.5 to $2 \mu m$.
- 12. (original) The device according to Claim 1 wherein said seed metal comprises copper.
- 13. (original) The device according to Claim 1 wherein said seed metal overlaps said second inorganic layer by an amount between about 5 and 15 μ m.
- 14. (original) The device according to Claim 1 wherein said buffer metal comprises a single metal layer.
- 15. (original) The device according to Claim 14 wherein said single metal layer comprises copper or a copper alloy.
- 16. (original) The device according to Claim 1 wherein said buffer metal comprises a stack of metal layers.
- 17. (original) The device according t Claim 16 wherein said stack of metal layers comprises copper in contact with said seed metal, nickel on top of said copper, and palladium as outermost metal.
- 18. (original) The device according to Claim 1 further comprising a metal reflow element attached to said buffer metal.
- 19. (original) The device according to Claim 1 further comprising a bond wire attached to said buffer metal.

Appl. No.10/689,386 Amdt. dated Feb. 23, 2005 Reply to Office action of Nov. 30, 2004

20. (original) A semiconductor device including a contact pad and circuit metallization on the surface of an integrated circuit chip, comprising:

a stack of protection layers over the surface of said chip, said stack comprising a first inorganic layer on said surface, a polymer layer on said first inorganic layer, and a second inorganic layer on said polymer layer;

a window in said stack of layers exposing said metallization on said integrated circuit chip;

a seed metal layer on said metallization in said window and on said second inorganic layer, said seed metal layer patterned to form an extended trace remote from said window; and

a patterned buffer metal layer positioned on a selected location of said seed metal layer.

- 21. (original) The device according to Claim 20 further comprising a metal reflow element attached to said buffer metal.
- 22. (original) The device according to Claim 20 further comprising a bond wire attached to said buffer metal.

23-28. (canceled)